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✓ **MEMORY Storage**

- Integrated with a 1Gb DDR2 die which is up to 1066Mbps
- OTP ROM 64bit Chip ID

✓ **DMA CONTROLLER**

- 8 physical channels and 4 bus channels
- Stride mode support
- Software configurable priority

✓ **Boot ROM**

- On chip boot ROM with boot loader
- The system could be loaded from SPI Nor flash

✓ **USB 2.0 OTG**

- Complies with Universal Serial Bus Specification. Revision 2.0.
- Complies with On-The-Go Supplement to the USB2.0 Specification Revision 1.0a.
- Supports point-to-point communication with one low-speed, full-speed or high-speed device in Host mode.
- Supports full-speed or high-speed in peripheral mode.
- Supports USB Mass Storage Class Bulk-Only Transport Revision 1.0 as host or device.
- Supports Electronic still picture imaging Picture Transfer Protocol (PTP)
- Supports direct print function using pict-bridge
- Supports Universal Serial Bus Device Class Definition for Printing Devices Version 1.1 as host
- Supports Universal Serial Bus Still Image Capture Device Definition Revision 1.0 as host
- Configurable/programmable size of endpoints.
- Configurable/programmable single, double, triple or quad buffering.
- Programmable type of endpoints.
- Supports high-speed high-bandwidth Isochronous and Interrupt transfer.
- Supports suspend, resume and power managements function.
- Support USB wakeup

✓ **OTHER INTERFACE**

- UART/I2C/SPI
- 3 external interrupts
- 41 configurable GPIO shared with function pins

✓ **POWER**

- 1.3v for core

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7 Crystal Requirements

Requirements for 24MHz oscillator.

Description	Specification Requirement
Nominal Frequency	24MHz
Oscillation Mode	Fundamental
Frequency Tolerance at 25	± 30ppm
Temperature Stability	± 50ppm

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